

INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)	Application Number		10585390	
	Filing Date		2009-06-22	
	First Named Inventor	Furrer		
	Art Unit	2416		
	Examiner Name			
	Attorney Docket Number	CH920030062US1		

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/J.B./	1	6512798		2003-01-28	Akiyama	

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/J.B./	1	0668678	EP	A2	1995-08-23	Cariolaro		<input type="checkbox"/>
/J.B./	2	2001308815	JP		2001-11-02	Kaneko	Abstract	<input checked="" type="checkbox"/>
/J.B./	3	7226724	JP		1995-08-22	Seki	Abstract	<input checked="" type="checkbox"/>

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**
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Attorney Docket Number	CH920030062US1	

4	11163953	JP		1999-06-18	Iwaki	Abstract	<input checked="" type="checkbox"/>
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/J.B./	1	MATUSIAK, "Implementing Fast Fourier Transform Algorithms of Real-Valued Sequences with the PTS320 DSP Family", Texas Instruments Application Report SPRA291, Dec. 1997, pp 12-19, 30-40, http://www.eetkorea.com/Articles/2001May/2001May07_DSP_EMS_AN.PDF .	<input type="checkbox"/>
/J.B./	2	HIROSAKI, "An Orthogonally Multiplexed QAM System Using the Discrete Fourier Transform", IEEE Transaction on Communications, July 1981, Vol. 29, No. 7, pages 982-989.	<input type="checkbox"/>
/J.B./	3	CHU, "Inside the FFT black box", CRC Press, 2000, Chapter 14, http://www.engnetbase.com/books/735/0270_pdf_toc.pdf	<input type="checkbox"/>

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